

### Amendments to the Specification

**Please replace the paragraph beginning at page 4, line 7, with the following rewritten paragraph:**

In the present invention, signal generation means generates a second clock signal having a frequency of an integral multiple of a frequency of a first clock signal; data value acquisition means acquires a data value of a data signal based on timing of the second clock signal generated by high frequency clock signal generation means; clock component extraction means extracts a component of the first clock signal from the data value acquired by the data value acquisition means; zero cross detection means detects, as a zero cross point, a timing at which a magnitude relationship with respect to a predetermined criterion level is inverted for the component of the first clock signal extracted by the clock component extraction means;—and symbol position detection means detects, as a symbol position of the data signal, a time at which a time period corresponding to the predetermined number of clocks for the second clock signal is elapsed from the zero cross point detected by the zero cross detection means; and the zero cross detection means detects, as the zero cross point, a clock whose position corresponding to a timing closest to the timing at which the magnitude relationship with respect to the predetermined criterion level is inverted for the first clock component extracted by the clock component extraction means.